Piezo-sounder Driver with Multi-mode charge pump

FEATURES

- Supply Voltage Range from 1.3 V to 5.5V
- 18V_{PP} Output from a 1.3V Supply
- Integrated Boost Converter Generates up to 16.5V Supply
- Input Signal 20Hz to 10kHz
- No Voltage Cross Output at Shutdown Mode
- Low Current Consumption
- Automatic Standby and Wake-up Control
- Available QFN16 and QFN12 package
- Short protection current about 100mA
- OTP feature

APPLICATIONS

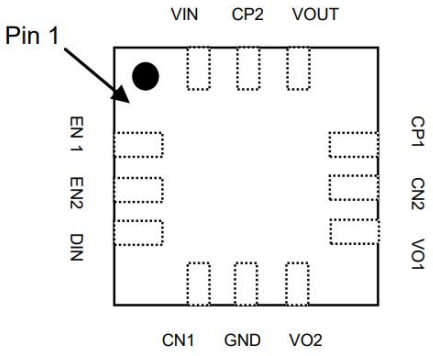
- Health Care Systems
- Home Appliances
- Wrist Watches
- Handheld GPS devices
- PDAs
- Security Devices
- Alarm Clocks

DESCRIPTION

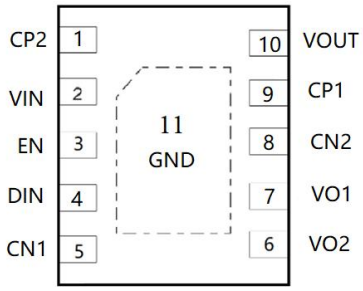
The SD110 is a switching driver with multi-mode charge pump for piezo-sounder. It can drive outputs up to 18Vpp from 1.3V supply. For adjusting the piezoelectric sounder sound volume, the charge pump can operate in either of a 1x, 2x or 3x mode. Because SD110 has the shutdown function, it is suitable for the battery application.

SD110 includes built-in automatic shutdown and wake up that guarantees longer battery life. SD110 features thermal shutdown and output short protection circuits.

PACKAGE (QFN16 AND QFN12)



QFN3*3_ 12



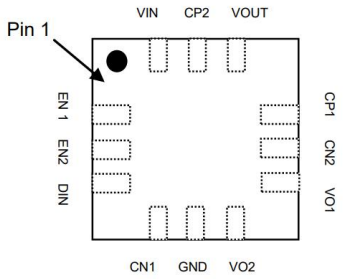
DFN2*2 _ 10

ORDING INFORMATION

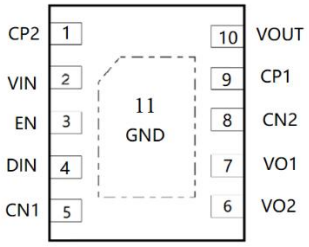
Part Number	Package Type	Package Qty	Op Temp(°C)	Mark
SD110	QFN12	5000	-40~85	SD110 XXX
SD110	DFN10	3000	-40~85	SD110 XXX

Piezo-sounder Driver with Multi-mode charge pump

PINOUT



QFN3*3_12



DFN2*2_10

PIN FUNCTIONS

(1)G = Ground, I = Input, O = Output, P = Power

Pin Number		Pin Name	Type	Function
DFN2*2_10	QFN3*3_12			
3	1	EN1	I	Charge pump mode select 1
	2	EN2	I	Charge pump mode select 2
4	3	DIN	I	Signal Input
5	4	CN1	I	Capacitor 1 Negative Terminal
	5	GND	P	Ground
6	6	VO2	O	Positive Output
7	7	VO1	O	Negative Output
8	8	CN2	I	Capacitor 2 Negative Terminal
9	9	CP1	I	Capacitor 1 Positive Terminal
12		NC	—	No Connection
10	10	VOUT	O	Boost Output
1	11	CP2	I	Capacitor 2 Positive Terminal
2	12	VIN	P	Power supply
11		EPAD	—	GND

ABSOLUTE MAXIMUM RATINGS (NOTE 1)

(@ T_A = +25°C, unless otherwise specified.)

Symbol	Characteristics	Value	Unit
VIN	Supply Voltage	-0.3 to 6.0	V
VOUT	Output Voltage	-0.3 to 16.5	V
VEN1	EN1 Voltage	-0.3 to V_{IN} +0.3	V
TA	Operating Free-Air Temperature Range	-40 to +85	°C
T_J	Operating Junction Temperature Range	-40 to +150	°C
TSTG	Storage Temperature Range	-65 to +150	°C

Note: 1. Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

RECOMMENDED OPERATING CONDITIONS

(@ T_A = +25°C, unless otherwise specified.)

Symbol	Characteristics	Conditions	Min	Max	Unit
VIN	Supply Voltage	1x Mode, 2x Mode,3x Mode	1.3	5.5	V

ESD Susceptibility

HBM	Human Body Model	2	kV
MM	Machine Model	600	V
CDM	Charged Device Model	1	kV

THERMAL INFORMATION

Parameter	Symbol	Package	Maximum	Unit
Thermal Resistance (Junction to Ambient)	θ_{JA}	DFN2*2_10	68	°C/W
Thermal Resistance (Junction to Case)	θ_{JC}	DFN2*2_10	25	°C/W
Thermal Resistance (Junction to Ambient)	θ_{JA}	QFN12L	68	°C/W
Thermal Resistance (Junction to Case)	θ_{JC}	QFN12L	25	°C/W

ELECTRICAL CHARACTERISTICS

(@T_A = +25°C, V_{IN} = 3.0V, C_{PIEZO} = 30nF, f_{DIN} = 4 kHz, unless otherwise specified.)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Output Voltage	VOUT1	1x Mode	1.2	—	3	V
	VOUT2	2x Mode	5.2	—	6	V
	VOUT3	3x Mode (Note 2)	7.2	—	9.1	V
Operating Current 1	IDD11	1x Mode, C_{PIEZO} = No Load	—	105	—	μA
	IDD12	2x Mode, C_{PIEZO} = No Load	—	249	—	μA
	IDD13	3x Mode, C_{PIEZO} = No Load	—	354	—	μA
Operating Current 2	IDD21	1x Mode, Single-ended application	—	0.38	—	mA
	IDD22	2x Mode, Single-ended application	—	1.33	—	mA
	IDD23	3x Mode, Single-ended application	—	2.8	—	mA
Operating Current 3	IDD31	1x Mode, Differential application	—	1.1	—	mA
	IDD32	2x Mode, Differential application	—	4.46	—	mA
	IDD33	3x Mode, Differential application	—	9.83	—	mA
Shutdown Current	ISD	DIN = 0V	—	7	20	nA
Input Frequency	fIN	Rectangular pulse	—	3	—	kHz
Oscillating Frequency	f_{OSC}	—	—	200	—	kHz
VOUT Start Delay Time	t_{ON1}	1x Mode, From DIN signal High to 90% V_{OUT} steady state	—	95	—	μs
	t_{ON2}	2x Mode, From DIN signal High to 90% V_{OUT} steady state	—	310	—	μs
	t_{ON3}	3 x Mode From DIN signal High to 90% V_{OUT} steady state	—	390	—	μs
Shutdown Delay Time	t_{OFF}	DIN = H- > L	—	42	—	ms
Output Short-Circuit Current	ISC	—	—	40	—	mA
Control Terminal Voltage H	VIH	EN1, EN2, DIN pins	0.8*V_{IN}	—	VIN	V
Control Terminal Voltage L	VIL	EN1, EN2,DIN pins	0	—	0.2*V_{IN}	V
Control Terminal Current 1	IIH1	DIN = 3V	—	1.7	—	μA
Control Terminal Current 2	IIH2	V_{EN1} = 3V, DIN = 3V	—	1.7	—	μA
Control Terminal Current 3	IIH3	V_{EN1} = 3V, DIN = 0V	—	—	1	μA

CHARGE PUMP MODE SETTING

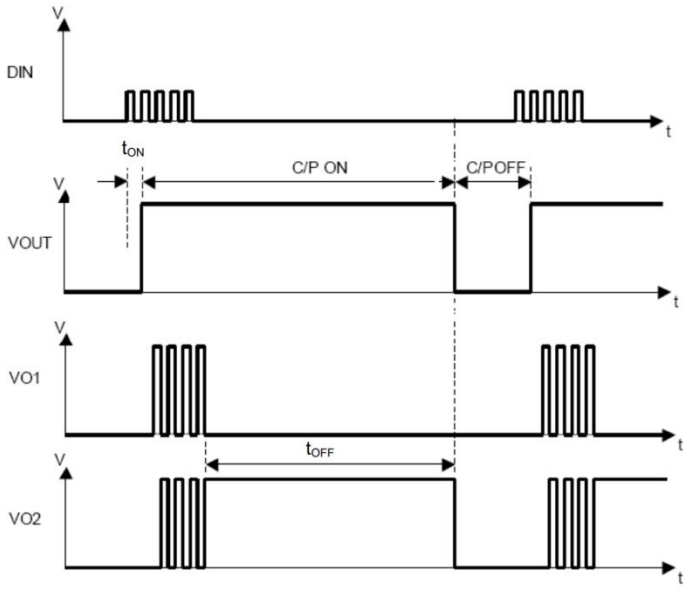
QFN3*3_12 MODE SETTING

DIN	EN1	EN2	MODE
0	--	--	Shutdown Mode
1	0	0	Shutdown Mode
1	0	1	1x Mode
1	1	0	2x Mode
1	1	1	3x Mode

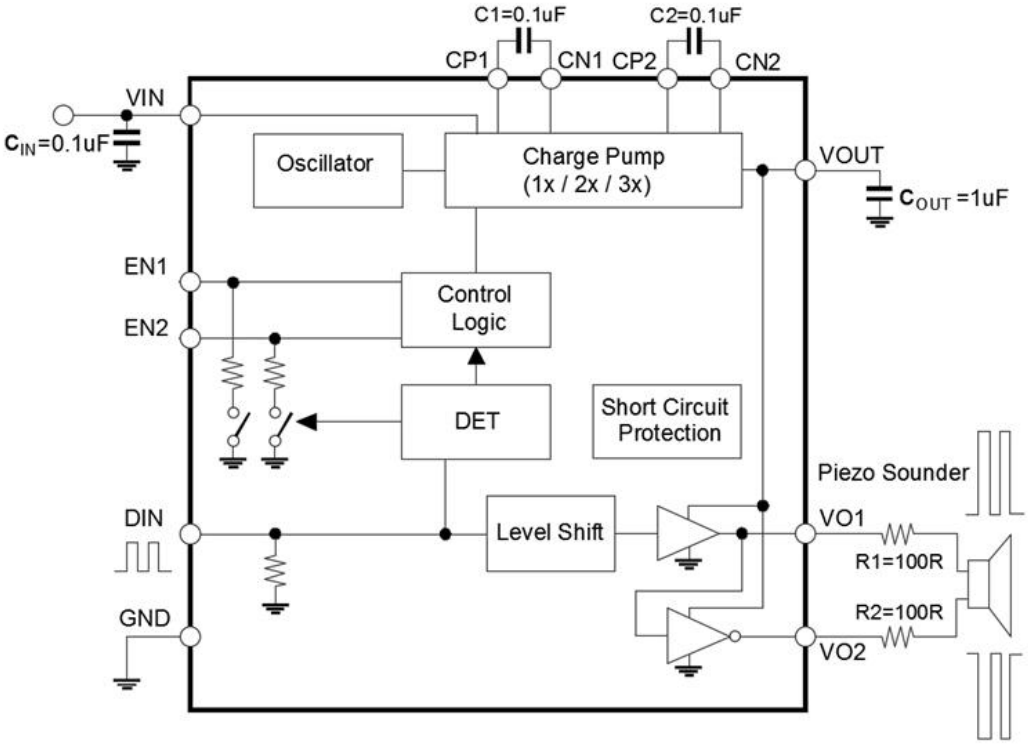
DFN2*2 MODE SETTING

DIN	EN	MODE
0	--	Shunt down Mode
1	0	2x mode
1	1	3x mode

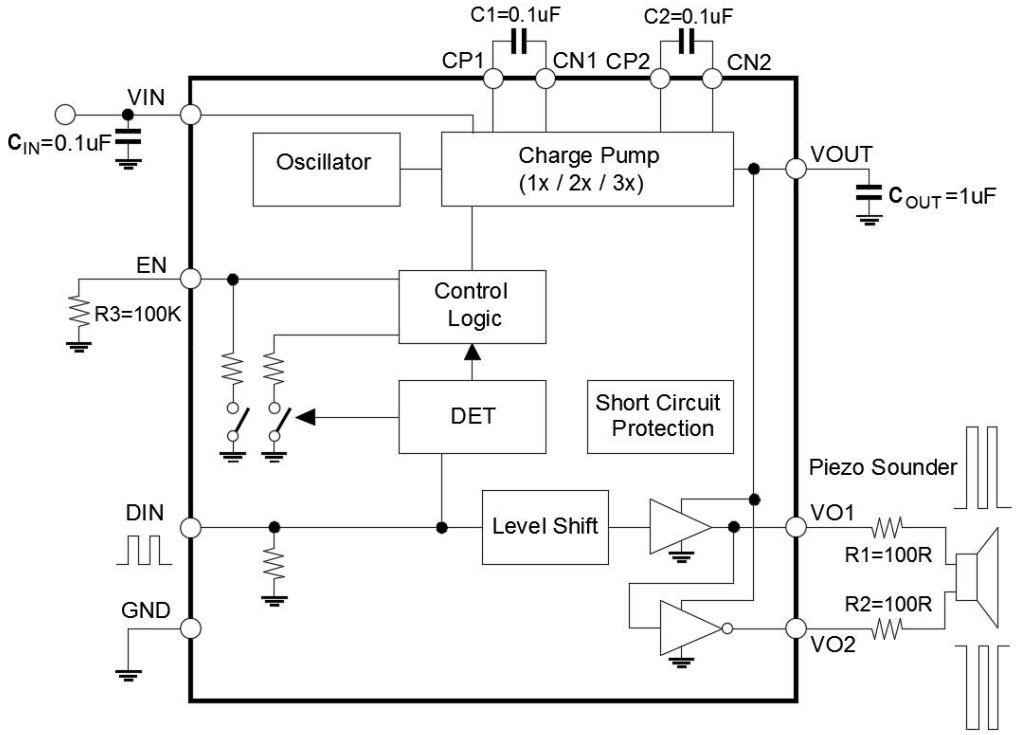
TIMING CHART



APPLICATION CIRCUIT

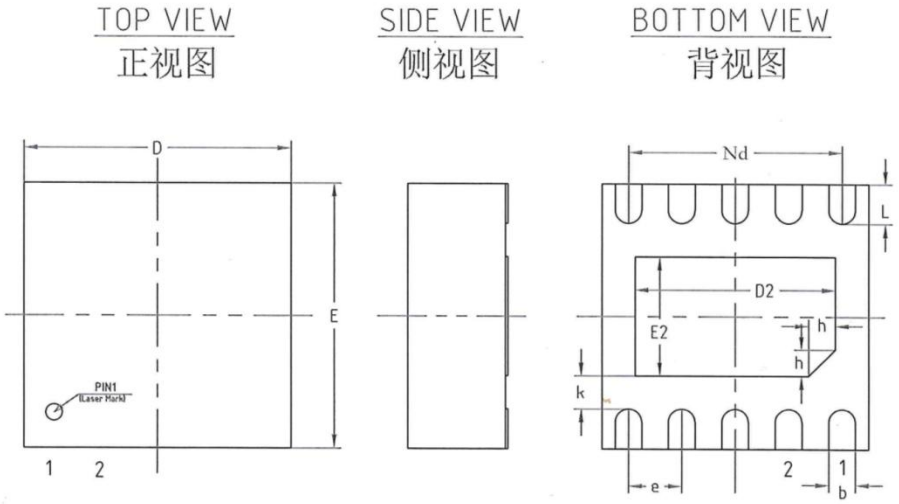


QFN3*3_12 Application

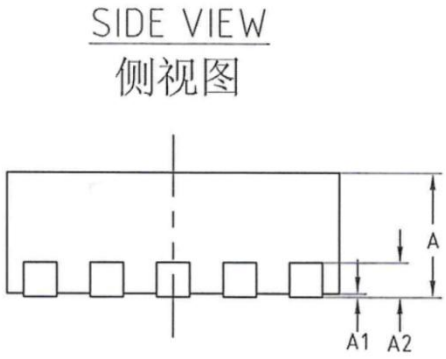


DFN2*2_10 Application

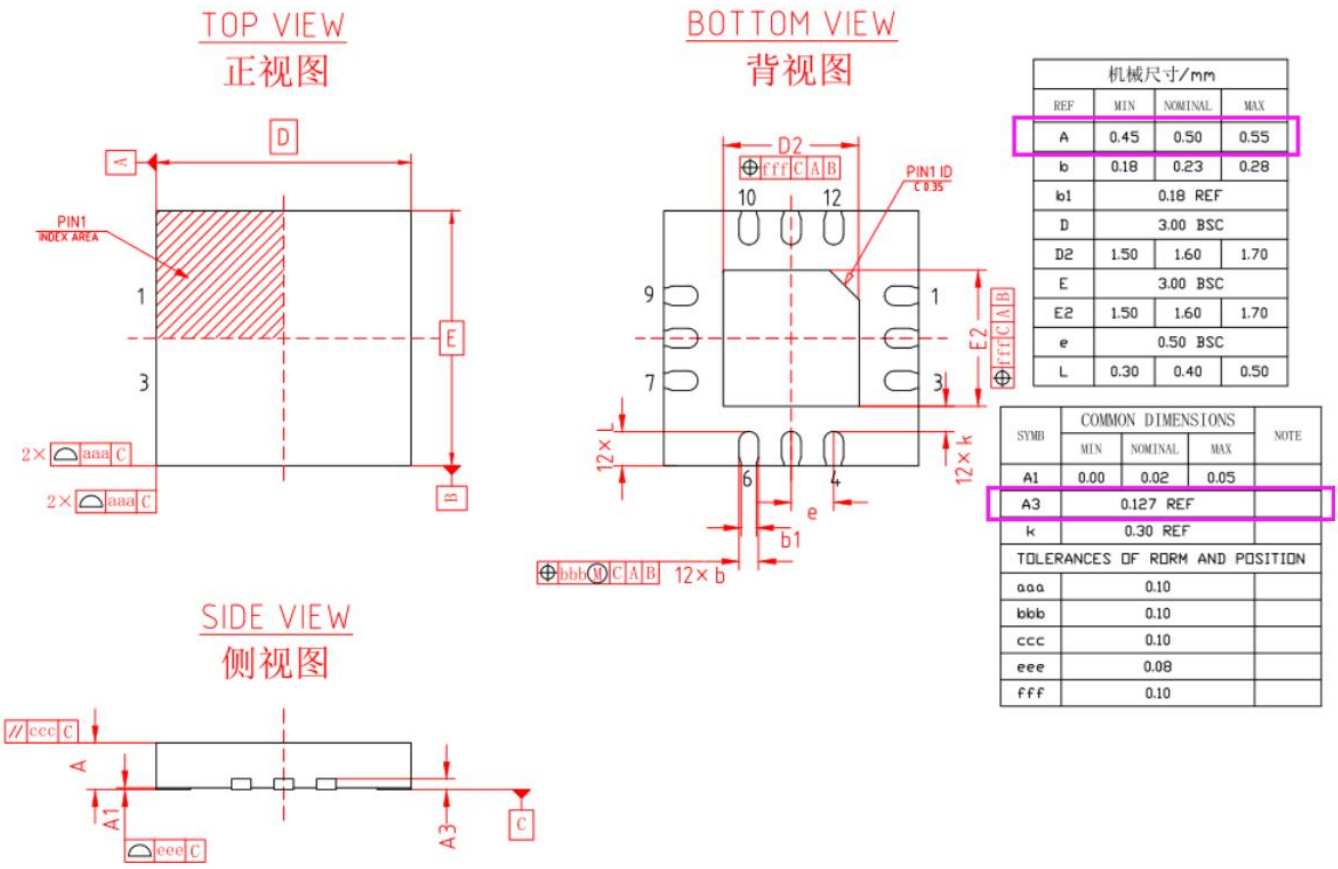
PACKAGE INFORMATION(DFN2*2_10)



机械尺寸/mm			
字符 SYMBOL	最小值 MIN	典型值 NOMINAL	最大值 MAX
A	0.70	0.75	0.80
A1	-	0.02	0.05
A2	0.203 REF		
b	0.15	0.20	0.25
D	1.90	2.00	2.10
D2	1.45	1.50	1.55
E	1.90	2.00	2.10
E2	0.85	0.90	0.95
e	0.40 BSC		
K	0.20	0.25	0.30
L	0.25	0.30	0.35
h	0.15	0.20	0.25
Nd	1.60 BSC		



PACKAGE INFORMATION(QFN3*3_12)



机械尺寸/mm			
REF	MIN	NOMINAL	MAX
A	0.45	0.50	0.55
b	0.18	0.23	0.28
b1		0.18 REF	
D		3.00 BSC	
D2	1.50	1.60	1.70
E		3.00 BSC	
E2	1.50	1.60	1.70
e		0.50 BSC	
L	0.30	0.40	0.50

SYMB	COMMON DIMENSIONS			NOTE
	MIN	NOMINAL	MAX	
A1	0.00	0.02	0.05	
A3		0.127 REF		
k		0.30 REF		
TOLERANCES OF RORM AND POSITION				
aaa		0.10		
bbb		0.10		
ccc		0.10		
eee		0.08		
fff		0.10		

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REVISION HISTORY

Document revision history

Data	Version	Changes
26-May-2024	Ver1.0	First issue DFN10 Spec
7-Aug-2024	Ver1.1	Application Circuit
17-Mar-2025	Ver1.2	Update the I_{SD} current