

#### **FEATURES**

- Meet Type-C discharge requirement
- Support Type-C 3A DFP port
- Fully Autonomous Type-C Controller Support Type-C Version 1.2
- 26 mΩ High-Side MOSFET
- 2uA supply current under idle state
- 4.2V ~ 6.5V Single Supply Operation.
- 2.0~4.0 A (typ.) Adjustable Current Limit
- Low Average Current in OUT shorted GND
- Support Apple® 2.7V/2.7V Fast Charging Mode
- Support Samsung® 1.2V/1.2V Fast Charging Mode
- Support BC1.2 & YD/T 1591-2009 Charging Mode
- Available ESOP8 package

#### APPLICATIONS

- USB Charger
- USB Wall Adapter
- Car Charger

controller optimized power chargers and mobile chargers. It broadcasts the available current of the charger over CC1/CC2 using the USB Type-C standard and prevents VBUS from being asserted until a valid connection has been verified. It can be used for up to 16.5W charging using Type-C protocols.

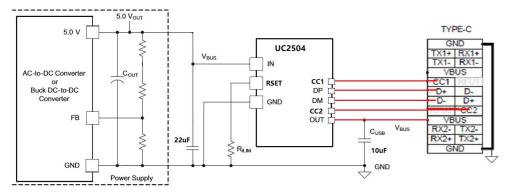
The UC2504 integrated USB charger emulators with a utomatic host charger identification circuitry and high performance adjustable current limiting power switch. An automatic USB charger identification circuit allow s mobile power supply can automatically provides the correct modes on the data lines to charger compliant d evices among the Apple, Samsung and BC1.2 modes.

The UC2504 is a  $26m\Omega$  power switch intended for applications where heavy capacitive loads and short-circuits are likely to be encountered. This also provides hiccup mode when enters OTSD mode and meet the latest VBUS discharge time requirements.

#### **DESCRIPTION**

The UC2504 is an autonomous source only Type-C

### PACKAGE AND SIMPLIFIED APPLICATION



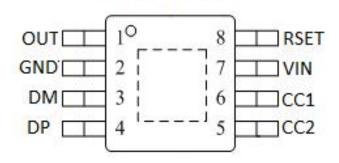
#### ORDING INFORMATION

Part Number	Package Type	Package Qty	Op Temp(°C)	Mark
UC2504	ESOP8	4000	-40~85	UC2504 XXX



## **PINOUT**

## UC2504 ESOP8



### **PIN FUNCTIONS**

NO.	NAME	TYPE(1)	DESCRIPTION
1	OUT	О	Power-switch output, connected to VBUS of USB; connect a 10μF or greater ceramic capacitor from OUT to GND as close to the IC as possible
2	GND	G	Ground connection
3	DM	O/I	DM data line to connector, input for hand-shake voltage from portable equipment high impedance while disabled
4	DP	O/I	DP date line to connector, output for hand-shake voltage to portable equipment, high impedance while disabled
5	CC2	O/I	Analog input/output that connects to the Type-C receptacle CC2 pin
6	CC1	O/I	Analog input/output that connects to the Type-C receptacle CC1 pin
7	VIN	P/I	Power supply/Input voltage connected to Power Switch; connect a $10\mu F$ or greater ceramic capacitor from IN to GND as close to the IC as possible
8	RSET	I	External resistor used to set current-limit threshold;

(1) G = Ground, I = Input, O = Output, P = Powe



## **ABSOLUTE MAXIMUM RATINGS (1)**

Over recommended operating free-air temperature range (unless otherwise noted)

PARAM	MIN	MAX	UNIT		
Supply Voltage Range	VIN	-0.3	7.0	V	
Input voltage range	DP,DM,CC1,CC2	-0.3	5.8	<b>V</b>	
	VIN, OUT		6	kV	
ESD rating, Human Body Model (HBM)	DP, DM, CC1,CC2		6		
Operating Junction Temperature	$T_{\rm J}$	-40	125	0.0	
Storage Temperature Range	$T_{ m stg}$	-65	150	°C	

<sup>(1)</sup> Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

#### THERMAL CHARACTERISTICS

over operating free-air temperature range (unless otherwise noted)

	UNIT			
$\theta_{ m JA}$	ESOP8 Package thermal impedance <sup>(1)</sup>	48		
$ heta_{ ext{JCtop}}$	Junction-to-case (top) thermal resistance	53	°C/W	
$ heta_{ ext{JCbot}}$	Junction-to-case (bottom) thermal resistance	13.5		
$\Theta_{ m JB}$	Junction-to-board thermal resistance	37		

<sup>(1)</sup>The package thermal impedance is calculated in accordance with JESD 51-7.

#### RECOMMENDED OPERATING CONDITIONS

PARAMETER		MIN	MAX	UNIT
V <sub>IN</sub>	Input voltage of IN	4.5	6.5	V
$V_{\mathrm{DP/DM}}$	DP data line input voltage		5.5	v
$I_{\mathrm{DP/DM}}$	Continuous sink/source current		±10	mA
R <sub>SET</sub>	Resistance of R <sub>SET</sub>	13	100	kΩ
I <sub>OUT</sub>	Continuous sink/source current	2000	4000	mA
T <sub>J</sub>	Operating Junction Temperature	-40	125	°C



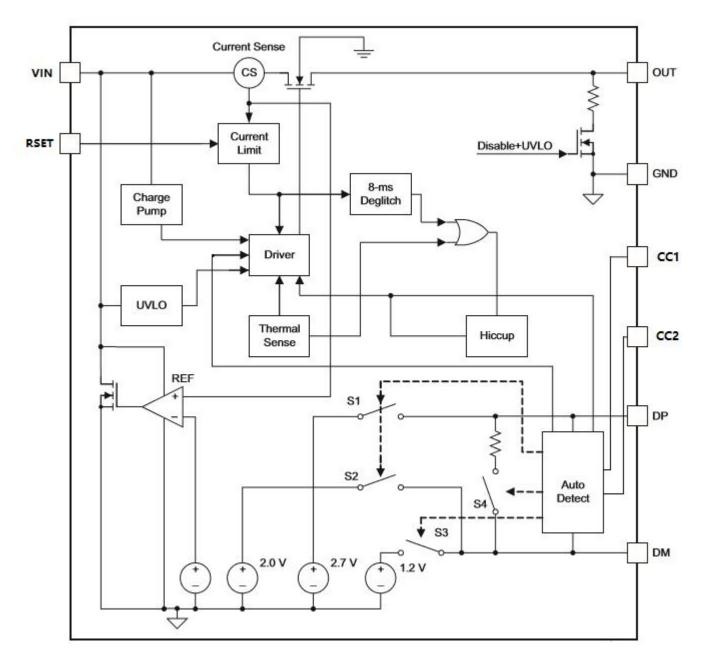
## **ELECTRICAL CHARACTERISTICS**

Conditions are:  $T_A = 25$ °C,  $V_{IN} = 5.0$  V and  $R_{SET} = 17.4$ k $\Omega$ . Positive current are into pins. All voltages are with respect to GND (unless otherwise noted).

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
Power Switch							
R <sub>DSON</sub>		I <sub>OUT</sub> =1.0A		26	28	mΩ	
		Current Limit					
BIN1 I <sub>OUT</sub>	OUT current limited	RSET=15.4K	3.1	3.3	3.5	A	
BIN2 Iout	OUT current limited	RSET=17K	3.1	3.3	3.5	A	
		SUPPLY CURRENT					
I <sub>IN</sub>	IN ON supply current			226			
I <sub>INI</sub>	IN idle supply current	CC1/CC2 floating		0	5	μΑ	
		BC 1.2 DCP MODE					
R <sub>DPM_SHORT</sub>	DP / DM shorting resistance			125	200	Ω	
	n	PAD MODE 2.4A Mode					
V <sub>DP_IPAD</sub>	DP output voltage		2.5	2.7	2.9	V	
V <sub>DM_IPAD</sub>	DM output voltage		2.5	2.7	2.9	V	
		Galaxy Tab MODE	·				
V <sub>DP_GAL</sub>	DP output voltage		1.1	1.2	1.3	17	
V <sub>DM_GAL</sub>	DM output voltage		1.1	1.2	1.3	V	
UFP Mode							
$I_{SRC}$	Sourcing current			326		uA	
Thermal Shutdown							
Totsd	Temperature Rising Threshold			150		°C	
	Hysteresis			20			



## **BLOCK FUNCTION DIAGRAM**





## PCB LAYOUT NOTIFICATION

Input capacitance C1 of the pin 7 of uc2504:

The voltage entering the pin 7 must pass through the input capacitor C1 at a single point(单点过电容), the C1 must be close to the pin 7.

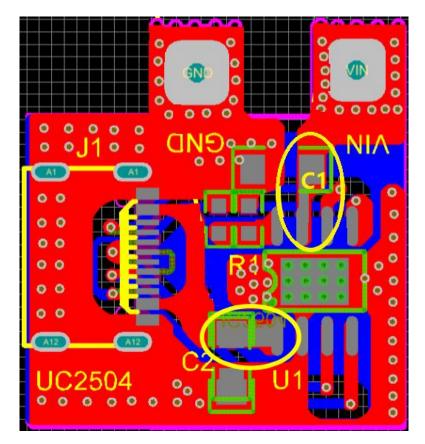
Output capacitance C2 of pin 1:

The pin1 to Type-C must pass through the output capacitor C2 and be close to pin 1.

Recommended capacitance C1 is 0805 size, value is 22uF;

Recommended capacitance C2 is 0805 size, value is 22uF;

Based the difference of AC/DC or DC/DC, the C1 and C2 can be bigger or smaller.





## **PACKAGE INFORMATION**

#### ESOP8

