

#### **FEATURES**

- Meet Type-C discharge requirement
- Support Type-C 3A DFP port
- Fully Autonomous Type-C Controller Support Type-C Version 1.2
- 26 mΩ High-Side MOSFET
- 2uA supply current under idle state
- $4.2V \sim 6.5V$  Single Supply Operation.
- 2.0~4.0 A (typ.) Adjustable Current Limit
- Low Average Current in OUT shorted GND
- Support Apple® 2.7V/2.7V Fast Charging Mode
- Support Samsung® 1.2V/1.2V Fast Charging Mode
- Support BC1.2 & YD/T 1591-2009 Charging Mode
- Available ESOP8 package

### APPLICATIONS

- USB Charger
- USB Wall Adapter
- Car Charger

### DESCRIPTION

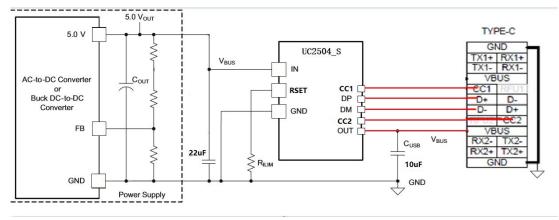
The UC2504 S is an autonomous source only Type-C

controller optimized power chargers and mobile chargers. It broadcasts the available current of the charger over CC1/CC2 using the USB Type-C standard and prevents VBUS from being asserted until a valid connection has been verified. It can be used for up to 16.5W charging using Type-C protocols.

The UC2504\_S integrated USB charger emulators wit h automatic host charger identification circuitry and hi gh performance adjustable current limiting power swit ch. An automatic USB charger identification circuit all ows mobile power supply can automatically provides t he correct modes on the data lines to charger complian t devices among the Apple, Samsung and BC1.2 mode s.

The UC2504\_S is a  $26m\Omega$  power switch intended for applications where heavy capacitive loads and short-circuits are likely to be encountered. This also provides hiccup mode when enters OTSD mode and meet the latest VBUS discharge time requirements.

#### PACKAGE AND SIMPLIFIED APPLICATION

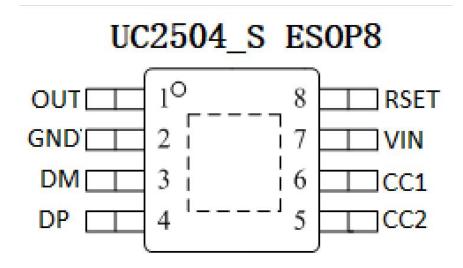


## **ORDING INFORMATION**

Part Number	Package Type	Package Qty	Op Temp(°C)	Mark
UC2504_S	ESOP8	4000	-40~85	UC2504_S XXX



### PINOUT



#### **PIN FUNCTIONS**

NO.	NAME	TYPE <sup>(1)</sup>	DESCRIPTION
1	OUT	О	Power-switch output, connected to VBUS of USB; connect a $10\mu$ F or greater ceramic capacitor from OUT to GND as close to the IC as possible
2	GND	G	Ground connection
3	DM	O/I	DM data line to connector, input for hand-shake voltage from portable equipment high impedance while disabled
4	DP	O/I	DP date line to connector, output for hand-shake voltage to portable equipment, high impedance while disabled
5	CC2	O/I	Analog input/output that connects to the Type-C receptacle CC2 pin
6	CC1	O/I	Analog input/output that connects to the Type-C receptacle CC1 pin
7	VIN	P/I	Power supply/Input voltage connected to Power Switch; connect a $10\mu$ F or greater ceramic capacitor from IN to GND as close to the IC as possible
8	RSET	Ι	External resistor used to set current-limit threshold;

(1) G = Ground, I = Input, O = Output, P = Powe



### **ABSOLUTE MAXIMUM RATINGS (1)**

Over recommended operating free-air temperature range (unless otherwise noted)

PARAM	ETER	MIN	MAX	UNIT	
Supply Voltage Range	VIN	-0.3	7.0	V	
Input voltage range	DP,DM,CC1,CC2	-0.3	5.8	v	
ESD anting Hammer Date Madel (HDM)	VIN, OUT		6	1-17	
ESD rating, Human Body Model (HBM)	DP, DM, CC1,CC2		6	kV	
Operating Junction Temperature	TJ	-40	125		
Storage Temperature Range	T <sub>stg</sub>	-65	150	°C	

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

### THERMAL CHARACTERISTICS

over operating free-air temperature range (unless otherwise noted)

	THERMAL METRIC (EMSOP8)		UNIT
$\theta_{JA}$	ESOP8 Package thermal impedance <sup>(1)</sup>	48	
$\theta_{JCtop}$	Junction-to-case (top) thermal resistance	53	0C/W
$\theta_{JCbot}$	Junction-to-case (bottom) thermal resistance	13.5	°C/W
$\Theta_{\mathrm{JB}}$	Junction-to-board thermal resistance	37	

(1)The package thermal impedance is calculated in accordance with JESD 51-7.

#### **RECOMMENDED OPERATING CONDITIONS**

	PARAMETER	MIN	MAX	UNIT
V <sub>IN</sub>	Input voltage of IN	4.5	6.5	v
V <sub>DP/DM</sub>	DP data line input voltage		5.5	v
I <sub>DP/DM</sub>	Continuous sink/source current		±10	mA
R <sub>SET</sub>	Resistance of R <sub>SET</sub>	13	100	kΩ
I <sub>OUT</sub>	Continuous sink/source current	2000	4000	mA
TJ	Operating Junction Temperature	-40	125	°C



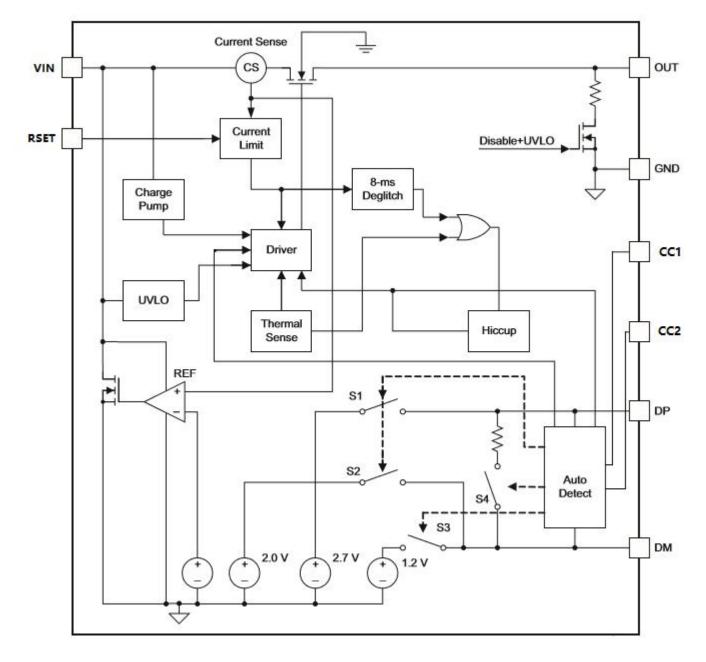
#### **ELECTRICAL CHARACTERISTICS**

Conditions are:  $T_A = 25^{\circ}$ C,  $V_{IN} = 5.0$  V and  $R_{SET} = 17.4$ k $\Omega$ . Positive current are into pins. All voltages are with respect to GND (unless otherwise noted).

	PARAMETER	TEST CONDITIONS	MIN	ТҮР	MAX	UNIT	
Power Switch							
R <sub>DSON</sub>		I <sub>OUT</sub> =1.0A		26	28	mΩ	
T <sub>DL</sub>	Power-on delay			0.46		S	
UVLO	Under voltage lock out			3.6		V	
	Hysteresis			0.3		V	
		Current Limit	·				
I <sub>OUT</sub>	OUT current limited	BIN1 RSET=15.4K	3.1	3.3	3.5	А	
I <sub>OUT</sub>	OUT current limited	BIN2 RSET=17.4K	3.1	3.3	3.5	А	
		SUPPLY CURRENT					
I <sub>IN</sub>	IN ON supply current			226			
I <sub>INI</sub>	IN idle supply current	CC1/CC2 floating		0	5	μΑ	
		BC 1.2 DCP MODE					
R <sub>DPM_SHORT</sub>	DP / DM shorting resistance			125	200	Ω	
	I	PAD MODE 2.4A Mode					
$V_{DP\_IPAD}$	DP output voltage		2.5	2.7	2.9	V	
V <sub>DM_IPAD</sub>	DM output voltage		2.5	2.7	2.9	V	
		Galaxy Tab MODE					
V <sub>DP_GAL</sub>	DP output voltage		1.1	1.2	1.3	17	
V <sub>DM_GAL</sub>	DM output voltage		1.1	1.2	1.3	V	
UFP Mode							
I <sub>SRC</sub>	Sourcing current			326		uA	
		Thermal Shutdown					
T <sub>otsd</sub>	Temperature Rising Threshold			150		°C	
	Hysteresis			20			



### **BLOCK FUNCTION DIAGRAM**





#### **PCB LAYOUT NOTIFICATION**

Input capacitance C1 of the pin 7 of UC2504\_S:

The voltage entering the pin 7 must pass through the input capacitor C1 at a single point(单点过电容), the C1 must be close to the pin 7.

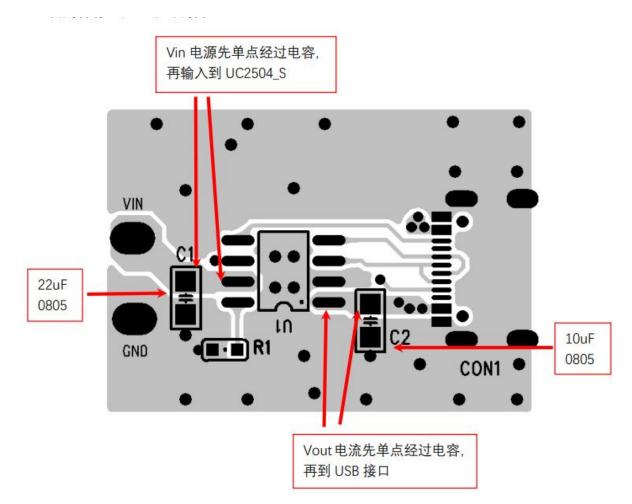
Output capacitance C2 of pin 1:

The pin1 to Type-C must pass through the output capacitor C2 and be close to pin 1.

Recommended capacitance C1 is 0805 size, value is 22uF;

Recommended capacitance C2 is 0805 size, value is 10uF;

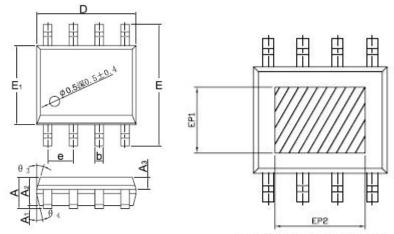
Based the difference of AC/DC or DC/DC, the C1 and C2 can be bigger or smaller.



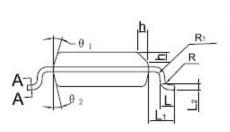


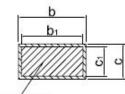
# **PACKAGE INFORMATION**

ESOP8



DIMENSIONS IN MUILLIMETERS





BASE METAL SECTIONA-A 6:1

SYMBOL	MIN	NOM	MAX
A	1,35	1.55	1,75
A	0.00	l	0,10
An	1,25	1,40	1,65
As	0,50	0,60	0,70
b	0,39		0,49
b.	0,28	Į.	0,48
c	0,10	Ţ	0,25
0	0,10	Ţ	0,23
D	4.80	4.90	5.00
E	5.80	6.00	6.20
E	3.80	3.90	4.00
		1.278SC	
i L	0,45	ĺ	1,00
Le	30000 M	1,04REF	
- La		0.25BSC	
R	0,07	l.	200
R	0,07	ļ	1
h	0,3	0,4	0,5
10.15	0*	ł	8°
H I	11°	17°	19°
ð	11*	13"	15*
8.	15°	17*	19*
10.1	11*	13*	15*
EP1	2.40	l	
EP2	3,30	<u> </u>	<u></u>