FEATURES

- Support BC1.2 USB-IF certification
- 4.5V~5.5V Single Supply Operation.
- Automatic USB charger Identification Circuit.
- UC2633B/UC2634B Support Apple® Devices fast charging. (Apple® 2.4A mode)
- Support Samsung Galaxy Tab Devices fast Charging. (Samsung® 2.1A mode)
- Support BC1.2 & YD/T 1591-2009 Charging Spec. (DCP® 1.0A mode)
- Available in SOT23-6 Package.

APPLICATIONS

Power Bank/Car Charger

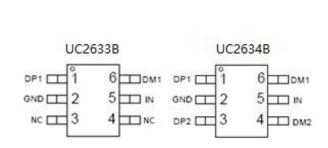
- USB Wall Adapter
- Travel Charger

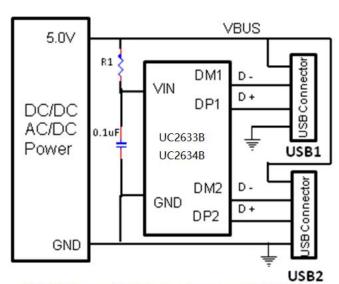
DESCRIPTION

The UC2633B/UC2634B is single/dual USB adapter emulators with automatic host charger identification circuitry for USB dedicated chargers.

The devices integrated automatic USB charger identification circuit allow mobile power supply, In-Car charger, USB wall adapters, travel chargers, and other dedicated chargers to identify themselves as a USB dedicated charger to USB devices, like Apple charger to Apple products, Samsung charger to Samsung Galaxy Tab & Smart Phone, and BC1.2 charger to HTC, SONY, LG, BlackBerry, Lenovo, Coolpad, ZTE, Huawei and other legacy D+/D- short detection devices.

PACKAGE AND APPLICATION





R1=1k in application to improve Reliability

PART NO. TABLE

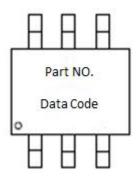
Part No.	Dual/Single	Apple 12W	Apple 10W	Apple 5W	SS 10W	DCP 5W
UC2633B	Single	Support			Support	Support
UC2634B	Dual	Support			Support	Support



ORDING INFORMATION

Part Number	Package Type	Package Qty	Op Temp(°C)
UC2633B	SOT23-6	3000	-40~85
UC2634B	SOT23-6	3000	-40~85

MARK INFORMATION



ABSOLUTE MAXIMUM RATINGS (1)

Over recommended operating free-air temperature range (unless otherwise noted)

	MIN	MAX	UNIT		
supply voltage range	IN	-0.3	6	17	
Input voltage range	DP1,DM1,DP2,DM2	-0.3	5.8	V	
Continuous output sink current	DP1,DP2 input current, DM1,DM2 input current		35		
Continuous output source current	DP1,DP2 output current, DM1,DM2 output current	urrent 35		mA	
ESD rating, Human Body Model	IN		8	1 7 7	
(HBM)	DP1,DP2,DM1,DM2		8	kV	
Operating Junction Temperature	T_J	-40	125		
Storage Temperature Range	$T_{ m stg}$	-65	150	°C	

⁽¹⁾ Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

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THERMAL CHARACTERISTICS

over operating free-air temperature range (unless otherwise noted)

	UNIT		
θ_{JA}	Package thermal impedance ⁽¹⁾	180	°C/W

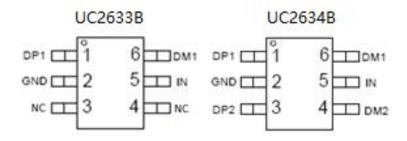
⁽¹⁾ The package thermal impedance is calculated in accordance with JESD 51-7.

RECOMMENDED OPERATING CONDITIONS

PARAMETER			MAX	UNIT
$V_{ m IN}$	Input voltage of IN	4.5	5.5	
V _{DP1/DP2}	D+ data line input voltage		5.5	V
V _{DM1/DM2}	D- data line input voltage		5.5	
I _{DP1/DP2}	Continuous sink/source current		±10	
I _{DM1/DM2}	Continuous sink/source current		±10	mA
T _J	Operating Junction Temperature	-40	125	°C



PINOUT



PIN FUNCTIONS

NO.	NAME	TYPE(1)	DESCRIPTION
1	DP1	O/I	DP date line to connector, output for hand-shake voltage to portable equipment, high impedance while disabled
2	GND	G	Ground connection
_	NC (UC2633B)	NC	No Connection
3	DP2 (UC2634B)	O/I	DP date line to connector, output for hand-shake voltage to portable equipment, high impedance while disabled
	NC (UC2633B)	NC	No Connection
DM2 (UC2634B)		O/I	DM data line to connector, input for hand-shake voltage from portable equipment high impedance while disabled
5	IN	P/I	Power supply/Input voltage connected to Power Switch; connect a 1 μ F or greater ceramic capacitor from IN to GND as close to the IC as possible
6	DM1	O/I	DM data line to connector, input for hand-shake voltage from portable equipment high impedance while disabled

⁽¹⁾ G = Ground, I = Input, O = Output, P = Power



ELECTRICAL CHARACTERISTICS

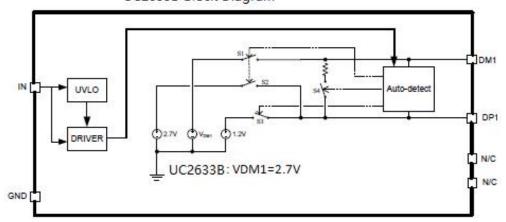
Conditions are -40°C \leq (T_J = T_A) \leq 125°C and 4.5 V \leq V $_{IN}$ \leq 5.5 V unless otherwise noted. Typical value is at 25°C. All voltages are with respect to GND unless otherwise noted.

	PARAMETER	TEST CONDITIONS	MIN	ТҮР	MAX	UNIT			
UNDERVOLTAGE LOCKOUT									
V _{UVLO}	IN rising UVLO threshold voltage		3.9	4.1	4.3	V			
	Hysteresis			100		mV			
	SUPPLY CURRENT								
$I_{\rm IN}$	IN supply current			160	300	μΑ			
	BC 1.2 DC	CP MODE (SHORT)		,					
R _{DPM_SHORT}	DP / DM shorting resistance	$V_{D^{+}} = 0.8V, I_{D^{-}} = 1 \text{mA},$		125	200	Ω			
Rdchg_short	Resistors connected DP /DM to GND after hand-shaking	$V_{D^+}=0.8V$		200	400	kΩ			
V _{DPL_TH_DETACH}	DP low threshold while detaching BC1.2 devices		310	330	350	mV			
V _{DPL_TH_DETACH_HYS}	hysteresis			50		mV			
	IPAD MOD	E(UC2633B/UC2634B)							
$V_{ ext{DP_IPAD}}$	DP1/DP2 output voltage	$V_{IN}=5.0V$	2.55	2.7	2.85	V			
V_{DM_IPAD}	DM1/DM2 output voltage	$V_{IN}=5.0V$	2.55	2.7	2.85	V			
R_{DP_IPAD}	DP1/DP2 output impedance	V_{IN} =5.0V , I_{D^+} = -5uA	20	30	40	kΩ			
R _{DM_IPAD}	DM1/DM2 output impedance	$V_{IN} = 5.0 V$, $I_{D-} = -5 u A$	20	30	40	kΩ			
Galaxy Tab MODE									
V_{DP_GAL}	DP1/DP2 output voltage	V _{IN} =5.0V	1.1	1.2	1.3				
$V_{\mathrm{DM_GAL}}$	V _{DM_GAL} DM1/DM2 output voltage		1.1	1.2	1.3	V			
R _{DP_GAL}	DP1/DP2 output impedance	V_{IN} =5.0V , I_{D^+} =-5uA	80	105	130	10			
R_{DM_GAL}	DM1/DM2 output impedance	$V_{IN}=5.0V$, $I_{D-}=-5uA$	80	105	130	kΩ			

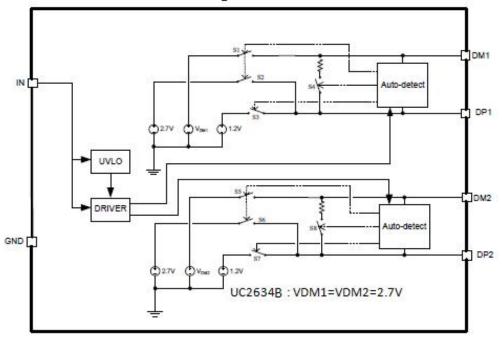


FUNCTIONAL BLOCK DIAGRAM

UC2633B Block Diagram



UC2634B Block Diagram





PACKAGE INFORMATION

SOT23-6

